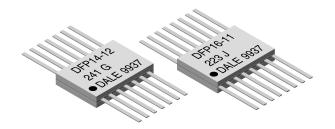
## Vishay Dale



## Thick Film Resistor Networks, Flat Pack



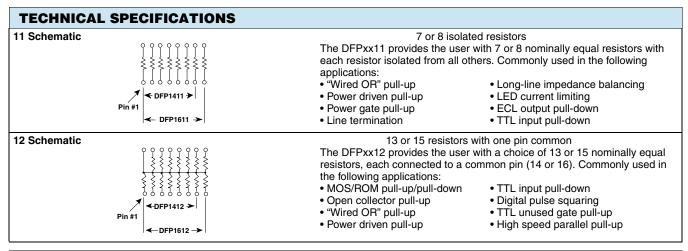
#### **FEATURES**

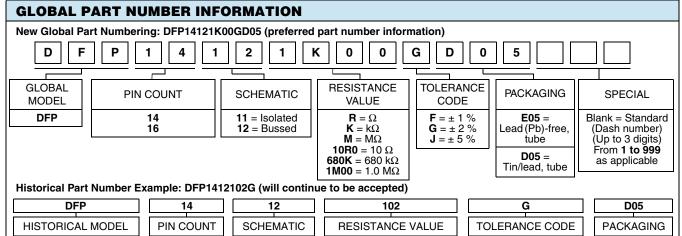
- Isolated and bussed schematics available
- 0.065" (1.65 mm) height for high density packaging
- Low temperature coefficient (- 55 °C to + 125 °C) ± 100 ppm/°C
- Hot solder dipped leads
- · Highly stable thick film
- Wide resistance range
- · All devices are capable of passing the MIL-STD-202, method 210, condition C "Resistance to Soldering Heat"

STANDARD ELECTRICAL SPECIFICATIONS								
POWER RATING			MAXIMUM	TEMPEDATURE (1)		RESISTANCE		
GLOBAL MODEL	ELEMENT P <sub>25 °C</sub>	PACKAGE P <sub>25 °C</sub>	CIRCUIT SCHEMATIC	WORKING VOLTAGE <sup>(3)</sup>	TEMPERATURE (1) COEFFICIENT	TOLERANCE (2) ± %	RANGE	TCR TRACKING ± ppm/°C
WODEL	W	W	CONLINATIO	V <sub>DC</sub>	± ppm/°C	Ξ /0	Ω	± ppiii/ 0
DFP	0.25	0.65	11	75	100	1, 2, 5	10 to 1M	50
	0.15	0.65	12	75	100	1, 2, 5	10 to 1M	50

#### **Notes**

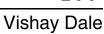
- Consult factory for stocked values
- (1) Temperature range: 55 °C to + 125 °C. (2) ± 2 % standard, ± 1 % and ± 5 % available.
- (3) Continuous working voltage shall be  $\sqrt{P} \times R$  or maximum working voltage, whichever is less.





Document Number: 31513

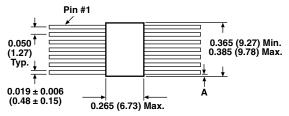
Revision: 03-Mar-10

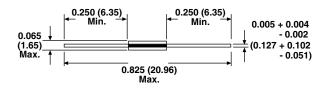




### Thick Film Resistor Networks, Flat Pack

### **DIMENSIONS** in inches (millimeters)



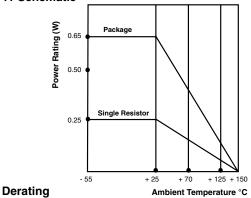


GLOBAL MODEL	DIMENSION A		
DFP14	0.037 ± 0.010 (0.94 ± 0.25)		
DFP16	$0.012 \pm 0.010 \ (0.30 \pm 0.25)$		

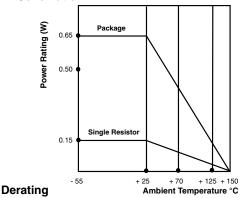
TECHNICAL SPECIFICATIONS					
PARAMETER	UNIT	DFP14/16			
Isolation resistance 11 schematic	ΜΩ	> 100			
Voltage coefficient of resistance	ppm/V	< 50 typical			
Maximum operating voltage	$V_{DC}$	75			
Operating temperature range	°C	- 55 to + 125			
Storage temperature range	°C	- 55 to + 150			

MECHANICAL SPECIFICATIONS					
Marking	Model number, schematic number, value tolerance, pin 1 indicator, date code				
Marking resistance to solvents	Permanency testing per MIL-STD-202, method 215				
Solderability	Per MIL-STD-202, method 208E				
Terminals	Per MIL-STD-1276 DFPxx11, DFPxx12 = type G (hot solder dipped). Hot solder dipped leads supplied as standard finish.				
Body	Epoxy filled ceramic sandwich				

#### 11 Schematic



#### 12 Schematic



PERFORMANCE					
TEST	CONDITIONS	MAX. $\Delta R$ (TYPICAL TEST LOTS)			
Power conditioning	1.5 x rated power, applied 1.5 h "ON" and 0.5 h "OFF" for 100 h ± 4 h at + 25 °C ambient temperature	± 0.50 % ΔR			
Thermal shock	5 cycles between - 65 °C and + 125 °C	± 0.50 % ΔR			
Short time overload	2.5 x rated working voltage, 5 s	± 0.25 % ΔR			
Low temperature operation	45 min at full rated working voltage at - 65 °C	± 0.25 % ΔR			
Moisture resistance	240 h with humidity ranging from 80 % RH to 98 % RH	± 0.50 % ΔR			
Resistance to soldering heat	Leads immersed in + 260° $\Delta$ C solder to within 1/16" of body for 10 s	± 0.25 % ΔR			
Shock	Total of 18 shocks at 100 g's	± 0.25 % ΔR			
Vibration	12 h at maximum of 20 g's between 10 Hz and 2000 Hz	± 0.25 % ΔR			
Load life	1000 h at + 70 °C, rated power applied 1.5 h "ON", 0.5 h "OFF" for full 1000 h period. Derated according to the curve.	± 0.50 % ΔR			
Terminal strength	1.5 pound pull for 30 s	± 0.25 % ΔR			
Insulation resistance	10 000 MΩ (minimum)	-			
Dielectric withstanding voltage	No evidence of arcing or damage (200 $V_{\mbox{\scriptsize RMS}}$ for 1 min)	-			

Document Number: 31513 Revision: 03-Mar-10 For technical questions, contact: ff2aresistors@vishay.com



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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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